MMM MMM		ннн ннн	ннн		RRRRRRRR	***************************************	LLL
MMM MMM	TTTTTTTTTTTTTTT	ннн	HHH		RRRRRRRR	TTTTTTTTTTTTTTT	LLL
ммммм ммммм	TTT	ннн	HHH	RRR	RRR	TTT	LLL
ммммм мммммм	TTT	ннн	HHH	RRR	RRR	TTT	LLL
ммммм мммммм	TTT	ннн	HHH	RRR	RRR	TTT	LLL
MMM MMM MMM	III	ннн	HHH	RRR	RRR	TTT	LLL
MMM MMM MMM	TTT	ННН	HHH	RRR	RRR	TTT	LLL
MMM MMM MMM	TTT	ннн	HHH	RRR	RRR	TTT	LLL
MMM MMM	TTT	нинининини			RRRRRRRR	TTT	LLL
MMM MMM	TTT	нинининини		RRRR	RRRRRRRR	TTT	LLL
MMM MMM	TTT	нинининини	нннн		RRRRRRRR	TTT	LLL
MMM MMM	TTT	ННН	HHH	RRR	RRR	TTT	LLL
MMM MMM	111	ннн	HHH	RRR	RRR	TTT	LLL
MMM MMM	III	ННН	HHH	RRR	RRR	TTT	LLL
MMM MMM	TTT	ННН	HHH	RRR	RRR	TTT	LLL
MMM MMM	TTT	ннн	HHH	RRR	RRR	TTT	LLL
MMM MMM	III	ннн	HHH	RRR	RRR	TTT	LLL
MMM MMM	TTT	ннн	HHH	RRR	RRR	TTT	LLLLLLLLLLLLLL
MMM MMM	TTT	ННН	HHH	RRR	RRR	TTT	LLLLLLLLLLLLLL
MMM MMM	TTT	ннн	HHH	RRR	RRR	TTT	LLLLLLLLLLLLLL

SYMIT MITTER MIT

000000 00 00 00 00		\$	DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD	VV	DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD
		\$			

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- D COMPLEX+16 P D COMPLEX+16 DIVISION R 16-SEP-1984 01:53:20 VAX/VMS Macro V04-00 OTS\$DIVCD Table of contents Page 0 HISTORY
DECLARATIONS
D COMPLEX*16 / D COMPLEX*16 giving D COMPLEX*16 result (2) (3) (4)

075

- D COMPLEX+16 / D COMPLEX+16 DIVISION R 16-SEP-1984 01:53:20 6-SEP-1984 11:27:34 VAX/VMS Macro V04-00 [MTHRTL.SRC]OTSDIVCD.MAR;1 (1) OTS\$DIVCD - D COMPLEX*16 / D COMPLEX*16 DIVISION ROUTINE /1-001/ ; File: OTSDIVCD.MAR COPYRIGHT (c) 1978, 1980, 1982, 1984 BY DIGITAL EQUIPMENT CORPORATION, MAYNARD, MASSACHUSETTS. ALL RIGHTS RESERVED. 10 11 12 13 14 15 16 17 18 THIS SOFTWARE IS FURNISHED UNDER A LICENSE AND MAY BE USED AND COPIED ONLY IN ACCORDANCE WITH THE TERMS OF SUCH LICENSE AND WITH THE INCLUSION OF THE ABOVE COPYRIGHT NOTICE. THIS SOFTWARE OR ANY OTHER COPIES THEREOF MAY NOT BE PROVIDED OR OTHERWISE MADE AVAILABLE TO ANY OTHER PERSON. NO TITLE TO AND OWNERSHIP OF THE SOFTWARE IS HEREBY TRANSFERRED. THE INFORMATION IN THIS SOFTWARE IS SUBJECT TO CHANGE WITHOUT NOTICE AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL EQUIPMENT CORPORATION. DIGITAL ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS SOFTWARE ON EQUIPMENT WHICH IS NOT SUPPLIED BY DIGITAL. FACILITY: MATH LIBRARY : ABSTRACT: Perform D COMPLEX*16 division

AUTHOR:

Steven B. Lionel, 12-July-1979

MODIFIED BY:

The 307 The 237 1 p.

OTS

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- D COMPLEX*16 / D COMPLEX*16 DIVISION R 16-SEP-1984 01:53:20 VAX/VMS Macro V04-00 Page 2 HISTORY; Detailed Current Edit History 6-SEP-1984 11:27:34 [MTHRTL.SRC]OTSDIVCD.MAR;1 (2)

0000 45 .SBTTL HISTORY; Detailed Current Edit History
0000 46
0000 47
0000 48; Edit History
0000 49;
0000 50
0000 51; 1-001 - Adapted from OTS$DIVC version 1-003. SBL 12-July-1979
```

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(3)

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- D COMPLEX*16 / D COMPLEX*16 DIVISION R 16-SEP-1984 01:53:20 VAX/VMS Macro V04-00 DECLARATIONS 6-SEP-1984 11:27:34 [MTHRTL.SRCJOTSDIVCD.MAR;1
         .SBTTL DECLARATIONS
                                     INCLUDE FILES:
                                     EXTERNAL SYMBOLS:
                                     MACROS:
                                    PSECT DECLARATIONS:
                                              .PSECT _OTS$CODE
                                                                                    PIC, USR, CON, REL, LCL, SHR, -
EXE, RD, NOWRT, LONG
                                     EQUATED SYMBOLS:
00000004
000000000
00000014
0000001C
                                                                                                 ; real part of dividend
; imag part of dividend
; real part of divisor
; imag part of divisor
                                                          = 4
= 12
= 20
= 28
                                     OWN STORAGE:
```

none

```
.SBTTL D COMPLEX*16 / D COMPLEX*16 giving D COMPLEX*16 result
FUNCTIONAL DESCRIPTION:
                                                                                OTS$DIVCD_R3 - D COMPLEX*16 / D COMPLEX*16 giving D COMPLEX*16 result
                                                                                 The COMPLEX*16 result is computed as follows:

    Let (a, b) represent the COMPLEX*16 dividend.
    Let (c, d) represent the COMPLEX*16 divisor.
    Let (r, i) represent the COMPLEX*16 quotient.

                                                                                Then:
                                                                                r = (ac + bd) / (cc + dd)
i = (bc - ad) / (cc + dd)
                                                      CALLING SEQUENCE:
                              108
                                                                               Complex_quotient.wdc.w = OTS$DIVCD_R3(dividend.rdc.v, divisor.rdc.v)
                                                       INPUT PARAMETERS:
                                                                               Dividend and divisor parameters are represented as FORTRAN D COMPLEX*16 numbers and are CALL BY VALUE. Passing 128 bit quantities by value is a violation of the VAX calling standard, but is excused because
                               115
                                                                                 this is a code support routine not meant to be
                                                                                callable by users.
                            119 II
120 II
121 II
1223 II
1224 II
1225 II
1226 II
1227 II
1230 II
1
                                                     IMPLICIT INPUTS:
                                                      OUTPUT PARAMETERS:
                                                                               NONE
                                                       IMPLICIT OUTPUTS:
                                                                               NONE
                                                      FUNCTIONAL VALUE:
                                                                               The D COMPLEX*16 value returned is (a, b) / (c, d) in resisters RO-R3! This is a violation of the VAX calling standard, but is excused because this is
                                                                                 a code support routine, not meant to be callable
                                                                                by users.
                                                      SIDE EFFECTS:
                                                                               Modifies registers RO-R3!
SS$_ROPRAND if either argument is a reserved operand.
SS$_FLTOVF if floating overflow
SS$_FLTDIV if divide by zero
```

- D COMPLEX*16 / D COMPLEX*16 DIVISION R 16-SEP-1984 01:53:20 VAX/VMS Macro V04-00 D COMPLEX*16 / D COMPLEX*16 giving D COM 6-SEP-1984 11:27:34 [MTHRTL.SRC]OTSDIVCD.MAR;1

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Page

075\$DIV	CD					- D	COMPLEX	(+16	/ D CC	MPLEX+16 D	F 6 IVISION R	16-SEP-1984 6-SEP-1984	01:53	:20 VAX/VI	1S Macro VO4-00		Page	5.
1-001						OFFO		144							IC.SRCJOISDIVCD IO.R11> math error hand			(5)
							0002	145		MTHSFLA	G_JACKET		: '	establish r	math error hand	ler		
		6D	000	0000	O°GF	9E	0002 0009 0009			MOVAB	G^MTH\$\$J/	ACKET_HND, (FP)	set handler handler	address to ja	cket		
							0009 0009 0009	146 147 148 149	; Per	form scali	ng of all	operands be	fore d	ivision				
	51 50	16	AC	08 08 51	07 07 50 03	EF B1 14	0009 000F 0015 0018	150 151		EXTZV EXTZV CMPW BGTR	#7, #8, 6 #7, #8, 6 R0, R1	(AP), R1 d(AP), R0		R1 = c(AP) (R0 = d(AP) (R0 = MAX ((exp> 0,1,3 (exp> 0,1,3 (exp>, d <exp>)</exp>	77		
			50	50 50 50	07 07 50 51 50 7 51	EFF B14 B8EC4	001A 001D 0020 0024	152 153 154 155	2\$:	BGTR MOVW MNEGB ROTL CLRL	#7, #8, 6 #7, #8, 6 R0, R1 2\$ R1, R0 R0, R0 #7, R0, F	RO			ng exponent 0, pating scale fa	377,376,	,,1	
		58 54 54	14 10 04 00	AC AC AC	50 50 50	65 65 65	0026 0028 0030 0035	156 157 158 159 160 161		MULD3 MULD3 MULD3 MULD3	RO, c(AP) RO, d(AP) RO, a(AP) RO, b(AP)		:	scale all o R8-R9 gets R10-R11 get a b	pperands c ts d			
			50	58A 5504 552 558A 5555 5555 5555 5555 5555 5555	5426A8558AA88558	65 60 64 64 64 66 66	003A 003E 0045 0048 004B 0051 0057 005A 005D	160 161 162 163 164 165 166 167 173 174 175 176		MULD3 ADDD2 MULD2 MULD2 SUBD2 MULD2 MULD2 ADDD2 DIVD2 DIVD2	R4, R8, R R2, R10, R6, R0 R10, R4 R8, R2 R4, R2 R8, R8 R10, R10 R10, R8 R8, R0 R8, R2	RO R6		R0 = ac R7 = bd R0 = ac+bd R4 = ad R2 = bc R2 = bc - ac R8 = cc R10 = dd R8 = cc + cc R0 = (ac+bc) R2 = (bc-ac				
						04	005D 005E 005E	175 176 177		RET .END				(RO-R1, R2-	-R3) = (r, i)			

```
- D COMPLEX*16 / D COMPLEX*16 DIVISION R 16-SEP-1984 01:53:20 VAX/VMS Macro V04-00 6-SEP-1984 11:27:34 [MTHRTL.SRC]OTSDIVCD.MAR;1
OTS$DIVCD
                                                                                                                                                                                    Page
Symbol table
                      =
                        0000000C
00000014
0000001C
                      =
                      =
MTHSSJACKET HND
                         *******
OTS$DIVCD_R3
                         00000000 RG
                                                                         Psect synopsis
PSECT name
                                               Allocation
                                                                            PSECT No.
                                                                                            Attributes
                                               00000000
0000005E
                                                                                     0.)
    ABS
                                                                                                                                                                  NOWRT NOVEC BYTE NOWRT NOVEC LONG
                                                                                                                                  LCL NOSHR NOEXE NORD
 OTSSCODE
                                                                                                        USR
                                                                                                                 CON
                                                                                                                                           SHR
                                                                                                                                                   EXE
                                                                                                                                                            RD
                                                                     Performance indicators !
Phase
                                                           CPU Time
                                     Page faults
                                                                                 Elapsed Time
                                                          00:00:00.08
00:00:00.65
00:00:00.67
00:00:00.00
00:00:00.55
00:00:00.01
00:00:00.02
00:00:01.98
Initialization
                                                                                 00:00:01.21
                                                                                 00:00:04.02
Command processing
Pass 1
                                                                                 00:00:00.00
Symbol table sort
Pass 2
Symbol table output
Psect synopsis output
                                                                                 00:00:00.04
                                                                                 00:00:00.11
Cross-reference output
                                                                                 00:00:00.00
Assembler run totals
The working set limit was 900 pages. 3071 bytes (6 pages) of virtual memory were used to buffer the intermediate code.
There were 10 pages of symbol table space allocated to hold 6 non-local and 1 local symbols. 237 source lines were read in Pass 1, producing 11 object records in Pass 2. 1 page of virtual memory was used to define 1 macro.
                                                                   Macro library statistics !
Macro library name
                                                                  Macros defined
```

O GETS were required to define O macros.

\$255\$DUA28:[SYSLIB]STARLET.MLB;2

There were no errors, warnings or information messages.

MACRO/ENABLE=SUPPRESSION/DISABLE=(GLOBAL, TRACEBACK)/LIS=LIS\$:OTSDIVCD/OBJ=OBJ\$:OTSDIVCD MSRC\$:MTHJACKET/UPDATE=(ENH\$:MTHJACKET)+MSRC

0

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